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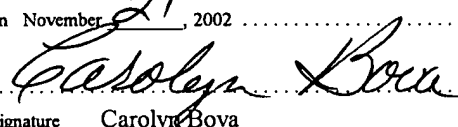
Docket No. 01-C-050

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): David C. McClure) Group Art Unit: 2818
Serial No.: 10/066,182) Examiner: Unknown
Filed: January 31, 2002)

For: CIRCUIT AND METHOD FOR TESTING A FERROELECTRIC MEMORY
DEVICE

To the Commissioner
for Patents
Washington, D.C. 20231

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231	
on November	21, 2002
	
Signature	Carolyn Bova

Dear Sir:

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement.

This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure.

The Examiner is respectfully requested to consider each of the cited references and, after such consideration, initial the enclosed Form PTO-1449 in the spaces provided next to each reference. Please return a copy of the initialed Form PTO-1449 with the next communication transmitted to the Applicants. For the convenience of the Examiner in considering the references, a copy of each of the cited references is enclosed with this communication. This statement is not to be construed as a representation that a search either has or has not been made.



CERTIFICATION

The undersigned hereby certifies under 37 C.F.R. §1.97(e)(2) that no item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in §1.56(c) more than three months prior to the filing of the information disclosure statement.

Any markings, underlines, notations or the like present in any reference are not to be construed as drawing the Examiner's attention to or away from other parts or portions of the references.

Respectfully submitted,

William F. Esser
Reg. No. 38,053

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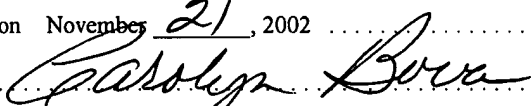
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Re: Applicant(s): David C. McClure
Serial No. 10/066,182
Filing Date: January 31, 2002
For: CIRCUIT AND METHOD FOR TESTING A FERROELECTRIC
MEMORY DEVICE
Docket No.: 01-C-050

Dear Sir:

Transmitted for filing with the Patent and Trademark Office are the following documents for the above-referenced patent application:

1. Supplemental Information Disclosure Statement;
2. Form PTO-1449;
3. Reference AA; and
4. Postcard acknowledgment.

Please address all communications related to this to:

William F. Esser, Esq.
STMicroelectronics, Inc.
1310 Electronics Drive
Carrollton, Texas 75006-5039

In the event there is an under or over payment, please debit or credit our Deposit Account #10-0447. This letter is being filed in duplicate to facilitate processing.

Respectfully submitted,



William F. Esser
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